



KLE Technological University
School of Electronics and Communication Engineering
BVB Campus, Vidyanagar, Hubballi, Karnataka - 580031

Ref. No:

Date:05-07-2023

ADVERTISEMENT FOR THE POST OF JUNIOR RESEARCH FELLOW (JRF) AND SENIOR PROJECT ASSOCIATE (SPA)

Applications are invited from eligible Indian nationals for the post of Junior Research Fellow (JRF) and Senior Project Associate (SPA) in a time bound C2S Category-I sponsored project undertaken at the School of Electronics and Communication Engineering of the KLE Technological University. The details are as follows.

Project Title	MATOMAC: Development of LPWAN ASIC
Funding Agency	The Ministry of Electronics and Information Technology is an executive agency of the Union Government of the Republic of India.
Temporary Positions	Junior Research Fellow (JRF) – 02 Senior Project Associate (SPA)-02
Consolidated monthly Fellowship	Rs. 37,000 + HRA, per month for 3 years for JRF Rs. 42,000 + HRA, per month for 3 years for SPA
Investigators	Dr. Saroja V. Siddamal and Dr. Suneeta V. BudihaI
Department/Centre	School of Electronics and Communication Engineering
Duration	03 years
Qualification and Experience	Qualification for the post of JRF: <ul style="list-style-type: none">• M.E/M.Tech with specialization in VLSI or embedded systems or communication or in related disciplines with consistently good academic record.• Candidates must have at least 60% (CGPA 6.5/10) marks in aggregate from a recognized technical institute or university.• Candidate should have qualified GATE at least once in his/her academic career. For the post of Senior Project Associate: <ul style="list-style-type: none">• M.E/M.Tech with specialization in VLSI or embedded systems or communication or in related disciplines with consistently good academic record.• Four years of experience in Research and Development in Industrial and academic institutions. <p style="text-align: center;">or</p> <p style="text-align: center;">Doctoral Degree in Engineering.</p> Experience for the post of JRF and SPA: <ul style="list-style-type: none">• The candidate needs to work in a wide range from specifications, circuit and architecture level, and finally leading to ASIC chip tapeout in latest CMOS technology node.• Good communication and written skills for technical documentation.
More information	Selected candidates are encouraged to register for PhD at KLE Technological University, Hubballi, provided the candidate completes the academic and administrative requirements.

Interested candidates are invited to email their detailed CV in the prescribed format along with the scanned copies of self-attested certificates, grade/mark sheets, publications etc., to careers@kletech.ac.in and sarojavs@kletech.ac.in. For more details, visit: www.kletech.ac.in/career on or before **August 14, 2023**.

The shortlisted candidates will be informed by E-mail/Phone. Selection will be based on qualification, experience and the performance in the interview. KLE Technological University, Hubballi reserves the right to reject any or all the application without assigning any reasons thereof.



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NAME						Recent Photo
ADDRESS						
EMAIL						
PHONE(MOBILE)						
Date of birth						
Gender						
Educational Qualifications						
Degree	Specialization	College and University	Marks/ CGPA	Class / Division	Year of passing	
10 th						
12 th						
B.E / B.Tech						
M.E/ M.Tech						
If any other						
GATE Score						
Any other National level Tests like NET						
Work experience (if any)						
Sl.no.	Duration	Organization	Designation	Nature of work		
Publication details (if any)						
Motivation to apply for this position						
Any other relevant information.						

All the above information provided by me is true to the best of my knowledge and I understand that, if found incorrect, I may be disallowed to appear for the interview/test or terminated at any stage.

Place :

Date:

Signature of the candidate